

| PIN | Type | Function | AM335x Pad Name | Alternate functions | GPIO | Description (refer to AM335x processor manuals for details) |
|--|------|----------|-----------------|--|----------|---|
| SSI 2 - Serial Audio Port 2 | | | | | | |
| 89 | NC | | | | | not connected |
| 90 | NC | | | | | not connected |
| 91 | NC | | | | | not connected |
| 92 | NC | | | | | not connected |
| 93 | NC | | | | | not connected |
| 94 | GND | GND | | | | |
| Secure Digital Interface 2 | | | | | | |
| 95 | NC | | | | | not connected |
| 96 | NC | | | | | not connected |
| 97 | NC | | | | | not connected |
| 98 | NC | | | | | not connected |
| 99 | NC | | | | | not connected |
| 100 | NC | | | | | not connected |
| 101 | NC | | | | | not connected |
| 102 | GND | GND | | | | |
| CMOS Sensor Interface | | | | | | |
| 103 | NC | | | | | not connected |
| 104 | NC | | | | | not connected |
| 105 | NC | | | | | not connected |
| 106 | NC | | | | | not connected |
| 107 | NC | | | | | not connected |
| 108 | NC | | | | | not connected |
| 109 | NC | | | | | not connected |
| 110 | NC | | | | | not connected |
| 111 | GND | GND | | | | |
| 112 | NC | | | | | not connected |
| 113 | NC | | | | | not connected |
| 114 | NC | | | | | not connected |
| 115 | NC | | | | | not connected |
| 116 | GND | GND | | | | |
| LCD Controller and Smart LCD Controller | | | | | | |
| 117 | 3V3 | LD0 | GPMC_AD8 | LCD_DATA23 MMC1_DAT0 MMC2_DAT4 EHRPWM2A PR1_MII_MT0_CLK | GPIO0_22 | LCD Data Bus |
| 118 | 3V3 | LD1 | GPMC_AD11 | LCD_DATA20 MMC1_DAT3 MMC2_DAT7 EHRPWM0_SYNC0 PR1_MII0_TXD3 | GPIO0_27 | LCD Data Bus |
| 119 | 3V3 | LD2 | GPMC_AD14 | LCD_DATA17 MMC1_DAT6 MMC2_DAT2 EQEP2_INDEX PR1_MII0_TXD0 PR1_PRU0_PRU_R31_14 | GPIO1_14 | LCD Data Bus |

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|-----|------|----------|-------------------|--|------------------------|---|
| 120 | 3V3 | LD3 | LCD_DATA11 | GPMC_A15 EHRPWM1B MCASP0_AHCLKR MCASP0_AXR2 PR1_MII0_RXD0 UART3_RTSN | GPIO2_17 10K-PD | LCD Data Bus SYS_BOOT11 |
| 121 | 3V3 | LD4 | LCD_DATA12 | GPMC_A16 EQEP1A_IN MCASP0_ACLKR MCASP0_AXR2 PR1_MII0_RXLINK UART4_CTSN | GPIO0_8 10K-PD | LCD Data Bus SYS_BOOT12 |
| 122 | 3V3 | LD5 | LCD_DATA13 | GPMC_A17 EQEP1B_IN MCASP0_FSR MCASP0_AXR3 PR1_MII0_RXER UART4_RTSN | GPIO0_9 10K-PD | LCD Data Bus SYS_BOOT13 |
| 123 | 3V3 | LD6 | LCD_DATA14 | GPMC_A18 EQEP1_INDEX MCASP0_AXR1 UART5_RXD PR1_MII_MRO_CLK UART5_CTSN | GPIO0_10 10K-PU | LCD Data Bus SYS_BOOT14 |
| 124 | 3V3 | LD7 | LCD_DATA15 | GPMC_A19 EQEP1_STROBE MCASP0_AHCLKX MCASP0_AXR3 PR1_MII0_RXDV UART5_RTSN | GPIO0_11 10K-PD | LCD Data Bus SYS_BOOT15 |
| 125 | 3V3 | LD8 | GPMC_AD9 | LCD_DATA22 MMC1_DAT1 MMC2_DAT5 EHRPWM2B PR1_MII0_COL | GPIO0_23 | LCD Data Bus |
| 126 | 3V3 | LD9 | GPMC_AD12 | LCD_DATA19 MMC1_DAT4 MMC2_DAT0 EQEP2A_IN PR1_MII0_TXD2 PR1_PRU0_PRU_R30_14 | GPIO1_12 | LCD Data Bus |
| 127 | 3V3 | LD10 | LCD_DATA5 | GPMC_A5 PR1_MII0_TXD0 EQEP2B_IN PR1_PRU1_PRU_R30_5 PR1_PRU1_PRU_R31_5 | GPIO2_11 10K-PD | LCD Data Bus SYS_BOOT5 |
| 128 | 3V3 | LD11 | LCD_DATA6 | GPMC_A6 PR1_EDIO_DATA_IN6 EQEP2_INDEX PR1_EDIO_DATA_OUT6 PR1_PRU1_PRU_R30_6 PR1_PRU1_PRU_R31_6 | GPIO2_12 10K-PU | LCD Data Bus SYS_BOOT6 |
| 129 | GND | GND | | | | |
| 130 | 3V3 | LD12 | LCD_DATA7 | GPMC_A7 PR1_EDIO_DATA_IN7 EQEP2_STROBE PR1_EDIO_DATA_OUT7 PR1_PRU1_PRU_R30_7 PR1_PRU1_PRU_R31_7 | GPIO2_13 10K-PD | LCD Data Bus SYS_BOOT7 |
| 131 | 3V3 | LD13 | LCD_DATA8 | GPMC_A12 EHRPWM1_TRIPZONE_IN PUT MCASP0_ACLKX UART5_TXD PR1_MII0_RXD3 UART2_CTSN | GPIO2_14 10K-PD | LCD Data Bus SYS_BOOT8 |

| PIN | Type | Function | AM335x Pad Name | Alternate functions | GPIO | Description (refer to AM335x processor manuals for details) |
|-----|------|----------|-------------------|---|------------------------|---|
| 132 | 3V3 | LD14 | LCD_DATA9 | GPMC_A13 EHRPWM0_SYNCO MCASP0_FSX UART5_RXD PR1_MII0_RXD2 UART2_RTSN | GPIO2_15 10K-PD | LCD Data Bus SYS_BOOT9 |
| 133 | 3V3 | LD15 | LCD_DATA10 | GPMC_A14 EHRPWM1A MCASP0_AXR0 PR1_MII0_RXD1 UART3_CTSN | GPIO2_16 10K-PD | LCD Data Bus SYS_BOOT10 |
| 134 | 3V3 | LD16 | GPMC_AD10 | LCD_DATA21 MMC1_DAT2 MMC2_DAT6 EHRPWM2_TRIPZONE_IN PUT PR1_MII0_TXEN | GPIO0_26 | LCD Data Bus |
| 135 | 3V3 | LD17 | GPMC_AD13 | LCD_DATA18 MMC1_DAT5 MMC2_DAT1 EQEP2B_IN PR1_MII0_TXD1 PR1_PRU0_PRU_R30_15 | GPIO1_13 | LCD Data Bus |
| 136 | 3V3 | LD18 | GPMC_AD15 | LCD_DATA16 MMC1_DAT7 MMC2_DAT3 EQEP2_STROBE PR1_ECAP0_ECAP_CAPIN _APWM_O PR1_PRU0_PRU_R31_15 | GPIO1_15 | LCD Data Bus |
| 137 | 3V3 | LD19 | LCD_DATA0 | GPMC_A0 PR1_MII_MT0_CLK EHRPWM2A PR1_PRU1_PRU_R30_0 PR1_PRU1_PRU_R31_0 | GPIO2_6 10K-PD | LCD Data Bus SYS_BOOT0 |
| 138 | 3V3 | LD20 | LCD_DATA1 | GPMC_A1 PR1_MII0_TXEN EHRPWM2B PR1_PRU1_PRU_R30_1 PR1_PRU1_PRU_R31_1 | GPIO2_7 20K-PU | LCD Data Bus SYS_BOOT1 (10K-PD if BOOTMODE = L) |
| 139 | 3V3 | LD21 | LCD_DATA2 | GPMC_A2 PR1_MII0_TXD3 EHRPWM2_TRIPZONE_IN PUT PR1_PRU1_PRU_R30_2 PR1_PRU1_PRU_R31_2 | GPIO2_8 10K-PD | LCD Data Bus SYS_BOOT2 (10K-PU if BOOTMODE = L) |
| 140 | 3V3 | LD22 | LCD_DATA3 | GPMC_A3 PR1_MII0_TXD2 EHRPWM0_SYNCO PR1_PRU1_PRU_R30_3 PR1_PRU1_PRU_R31_3 | GPIO2_9 10K-PD | LCD Data Bus SYS_BOOT3 (10K-PU if BOOTMODE = L) |
| 141 | 3V3 | LD23 | LCD_DATA4 | GPMC_A4 PR1_MII0_TXD1 EQEP2A_IN PR1_PRU1_PRU_R30_4 PR1_PRU1_PRU_R31_4 | GPIO2_10 10K-PU | LCD Data Bus SYS_BOOT4 |
| 142 | GND | GND | | | | |
| 143 | 3V3 | HSYNC | LCD_HSYNC | GPMC_A9 PR1_EDIO_DATA_IN3 PR1_EDIO_DATA_OUT3 PR1_PRU1_PRU_R30_9 PR1_PRU1_PRU_R31_9 | GPIO2_23 | |

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|-----|------|----------|-----------------------|---|----------|---|
| 144 | 3V3 | VSYNC | LCD_VSYNC | GPMC_A8 PR1_EDIO_DATA_IN2 PR1_EDIO_DATA_OUT2 PR1_PRU1_PRU_R30_8 PR1_PRU1_PRU_R31_8 | GPIO2_22 | |
| 145 | 3V3 | OE_ACD | LCD_AC_BIAS_EN | GPMC_A11 PR1_MII1_CRS PR1_EDIO_DATA_IN5 PR1_EDIO_DATA_OUT5 PR1_PRU1_PRU_R30_11 PR1_PRU1_PRU_R31_11 | GPIO2_25 | |
| 146 | 3V3 | LSCLK | LCD_PCLK | GPMC_A10 PR1_MII0_CRS PR1_EDIO_DATA_IN4 PR1_EDIO_DATA_OUT4 PR1_PRU1_PRU_R30_10 PR1_PRU1_PRU_R31_10 | GPIO2_24 | LCD Dotclock |
| 147 | GND | GND | | | | |

Module Specific Signals

| | | | | | | |
|-----|-----|------|----------|--|----------|--|
| 148 | 3V3 | GPIO | GPMC_A1 | GMII2_RXDV RGMII2_RCTL MMC2_DAT0 GPMC_A17 PR1_MII1_TXD3 EHRPWM0_SYNCO | GPIO1_17 | |
| 149 | 3V3 | GPIO | GPMC_A2 | GMII2_TXD3 RGMII2_TD3 MMC2_DAT1 GPMC_A18 PR1_MII1_TXD2 EHRPWM1A | GPIO1_18 | |
| 150 | 3V3 | GPIO | GPMC_A3 | GMII2_TXD2 RGMII2_TD2 MMC2_DAT2 GPMC_A19 PR1_MII1_TXD1 EHRPWM1B | GPIO1_19 | |
| 151 | 3V3 | GPIO | GPMC_A6 | GMII2_TXCLK RGMII2_TCLK MMC2_DAT4 GPMC_A22 PR1_MII1_RXD2 EQEP1_INDEX | GPIO1_22 | |
| 152 | 3V3 | GPIO | GPMC_A11 | GMII2_RXD0 RGMII2_RD0 RMII2_RXD0 GPMC_A27 PR1_MII1_RXER MCASP0_AXR1 | GPIO1_27 | |
| 153 | 3V3 | GPIO | GPMC_A10 | GMII2_RXD1 RGMII2_RD1 RMII2_RXD1 GPMC_A26 PR1_MII1_RXDV MCASP0_AXR0 | GPIO1_26 | |
| 154 | 3V3 | GPIO | GPMC_A5 | GMII2_TXD0 RGMII2_TD0 RMII2_TXD0 GPMC_A21 PR1_MII1_RXD3 EQEP1B_IN | GPIO1_21 | |
| 155 | 3V3 | GPIO | GPMC_A4 | GMII2_TXD1 RGMII2_TD1 RMII2_TXD1 GPMC_A20 PR1_MII1_TXD0 EQEP1A_IN | GPIO1_20 | |
| 156 | 3V3 | GPIO | GPMC_A0 | GMII2_TXEN RGMII2_TCTL RMII2_TXEN GPMC_A16 PR1_MII_MT1_CLK EHRPWM1_TRIPZONE_IN PUT | GPIO1_16 | |

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|-----|--------|----------|-----------------|--|----------|---|
| 157 | 3V3 | GPIO | GPMC_A7 | GMI12_RXCLK RGMII2_RCLK MMC2_DAT5 GPMC_A23 PR1_MII1_RXD1 EQEP1_STROBE | GPIO1_23 | |
| 158 | 3V3 | GPIO | GPMC_A8 | GMI12_RXD3 RGMII2_RD3 MMC2_DAT6 GPMC_A24 PR1_MII1_RXD0 MCASP0_ACLKX | GPIO1_24 | |
| 159 | | GPIO | GPMC_A9 | GMI12_RXD2 RGMII2_RD2 MMC2_DAT7 GPMC_A25 PR1_MII_MR1_CLK MCASP0_FSX | GPIO1_25 | |
| 160 | GND | GND | | | | |
| 161 | NC | | | | | not connected |
| 162 | NC | | | | | not connected |
| 163 | NC | | | | | not connected |
| 164 | NC | | | | | not connected |
| 165 | NC | | | | | not connected |
| 166 | NC | | | | | not connected |
| 167 | NC | | | | | not connected |
| 168 | NC | | | | | not connected |
| 169 | NC | | | | | not connected |
| 170 | NC | | | | | not connected |
| 171 | GND | GND | | | | |
| 172 | NC | | | | | not connected |
| 173 | NC | | | | | not connected |
| 174 | NC | | | | | not connected |
| 175 | NC | | | | | not connected |
| 176 | NC | | | | | not connected |
| 177 | NC | | | | | not connected |
| 178 | NC | | | | | not connected |
| 179 | 3V3 | | | | | Fixed function GPMI_CLE used for NAND flash |
| 180 | 3V3 | | | | | Fixed function GPMI_ALE used for NAND flash |
| 181 | 3V3 | | | | | Fixed function GPMI_WRN used for NAND flash |
| 182 | 3V3 | | | | | Fixed function GPMI_RDN used for NAND flash |
| 183 | GND | | VSSA_ADC | | | Connected to GND onboard |
| 184 | NC | | | | | not connected |
| 185 | analog | XN | AIN0 | | | Touscreen ADC input channels |
| 186 | analog | XP | AIN1 | | | |
| 187 | analog | YN | AIN2 | | | |
| 188 | analog | YP | AIN3 | | | |
| 189 | analog | WIPER | AIN4 | | | |

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|-----|--------|----------|-----------------|--|-------------------------------------|---|
| 190 | analog | | AIN5 | | | General purpose measurements channels |
| 191 | analog | | AIN6 | | | |
| 192 | analog | | AIN7 | | | |
| 193 | 1V8 | | EXT_WAKEUP | | | |
| 194 | NC | | | | | not connected |
| 195 | NC | | | | | not connected |
| 196 | NC | | | | | not connected |
| 197 | 3V3 | | EMU0 | | GPIO3_7 10K-PU to ETN_3V3 | Fixed function input used for Ethernet PHY Interrupt |
| 198 | 3V3 | | MDC | MDIO_CLK TIMER5 UART5_TXD UART3_RTSN MMC0_SDWP MMC1_CLK MMC2_CLK | GPIO0_1 | Fixed function ENET_MDC used for Ethernet PHY |
| 199 | 3V3 | | MDIO | MDIO_DATA TIMER6 UART5_RXD UART3_CTSN MMC0_SDCD MMC1_CMD MMC2_CMD | GPIO0_0 1K5-PU to ETN_3V3 | Fixed function ENET0_MDIO used for Ethernet PHY |
| 200 | GND | GND | GND | | | |